



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Jae-Eun LIM, et al.

Serial No.: 10/735,742

Group Art Unit: 2812

Filed: December 16, 2003

Examiner: Issac, Stanetta D

For: METHOD FOR MANUFACTURING SHALLOW TRENCH ISOLATION IN  
SEMICONDUCTOR DEVICE

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AMENDMENT

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 17, 2004

Sir:

In response to the outstanding Official Action dated September 9, 2004, please amend the subject application as follows: